

## Application Data Sheet

### **Application Information**

Application Type: Continuation

Subject Matter: Utility

Suggested Classification:

Suggested Group Art Unit:

CD-ROM or CD-R?: None

Title: Semiconductor Structure Having a Compensated Resistance in the LDD Area and Method for Producing the Same

Request for Early Publication?: No

Request for Non-Publication?: No

Suggested Drawing Figure: 1

Total Drawing Sheets: 3

Small Entity: No

Petition included?: No

Secrecy Order in Parent Appl.?: No

### **Applicant Information**

Applicant Authority type: Inventor

Primary Citizenship Country: Germany

Status: Full Capacity

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### **Representative Information**

Representative Designation:	Registration Number:	Representative Name:
Primary	37,892	Harold C. Moore
Primary	34,984	Paul J. Maginot
Primary	32,722	Michael D. Beck
Primary	34,214	David M. Lockman
Primary	31,993	David B. Quick
Primary	43,285	James D. Wood

### **Domestic Priority Information**

Application:	Continuity Type:	Parent Application:	Parent Filing Date:
	Continuation Of	PCT/EP02/09702	08/30/02

**Foreign Priority Information**

Country:	Application Number:	Filing Date:	Priority Claimed:
Germany	101 51 202.3	10/17/01	Yes

**Assignee Information**

Assignee name: Infineon Technologies AG

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